

ABSTRACT

The invention relates to a method and device for synthesising an electrical architecture. More specifically, the invention relates to a method of synthesising a routing which consists in obtaining the following parameters: the different configurations of service variants and calculator variants and the rate of occurrence of said configurations, the sum of the rates of the configurations being considered as equal to one; the component cost characteristics which are stored and weighted according to their respective rates of increase; and the partial or full positioning of the service variants on the calculator variants. The inventive method also consists in identifying valid routings, evaluating the routing cost of said valid routings for each configuration and determining the valid routing which minimizes the average of the routing costs for each configuration, said average being weighted by the rates of increase of each configuration.